Oracle® Communications
Core Session Manager – SCZ825m1

Program Documentation – License Document

First Edition

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Contents

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medium customarily used for software interchange.

  If distribution of object code is made by offering access to copy
from a designated place, then offering equivalent access to copy the
source code from the same place satisfies the requirement to
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compelled to copy the source along with the object code.

  5. A program that contains no derivative of any portion of the
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therefore falls outside the scope of this License.

  However, linking a "work that uses the Library" with the Library
creates an executable that is a derivative of the Library (because it
contains portions of the Library), rather than a "work that uses the
library". The executable is therefore covered by this License.
Section 6 states terms for distribution of such executables.

  When a "work that uses the Library" uses material from a header file
that is part of the Library, the object code for the work may be a

derivative work of the Library even though the source code is not.
Whether this is true is especially significant if the work can be
linked without the Library, or if the work is itself a library. The
threshold for this to be true is not precisely defined by law.

  If such an object file uses only numerical parameters, data
structure layouts and accessors, and small macros and small inline
functions (ten lines or less in length), then the use of the object
file is unrestricted, regardless of whether it is legally a derivative
work. (Executables containing this object code plus portions of the
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  Otherwise, if the work is a derivative of the Library, you may
distribute the object code for the work under the terms of Section 6.
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whether or not they are linked directly with the Library itself.

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uses the Library", as object code and/or source code, so that the user can modify the Library and then relink to produce a modified executable containing the modified Library. (It is understood that the user who changes the contents of definitions files in the Library will not necessarily be able to recompile the application to use the modified definitions.)

b) Use a suitable shared library mechanism for linking with the Library. A suitable mechanism is one that (1) uses at run time a copy of the library already present on the user's computer system, rather than copying library functions into the executable, and (2) will operate properly with a modified version of the library, if the user installs one, as long as the modified version is interface-compatible with the version that the work was made with.

c) Accompany the work with a written offer, valid for at least three years, to give the same user the materials specified in Subsection 6a, above, for a charge no more than the cost of performing this distribution.

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more than your reasonable cost of physically performing this
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protocols for communication across the network.

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 2. [2]Mark Andrews <mark_andrews@isc.org> Leitch atomic clock controller

 3. [3]Bernd Altmeier <altmeier@atlsoft.de> hopf Elektronik serial line and PCI-bus devices

 4. [4]Viraj Bais <vbais@mailman1.intel.com> and [5]Clayton Kirkwood <kirkwood@striderfm.intel.com> port to WindowsNT 3.5

 5. [6]Michael Barone <michael,barone@lmco.com> GPSVME fixes

 6. [7]Karl Berry <karl@owl.HQ.ileaf.com> syslog to file option

 7. [8]Greg Brackley <greg.brackley@bigfoot.com> Major rework of WINNT port. Clean up recvbuf and iosignal code into separate modules.

8. [9]Marc Brett <Marc.Brett@westgeo.com> Magnavox GPS clock driver

9. [10]Piete Brooks <Piete.Brooks@cl.cam.ac.uk> MSF clock driver, Trimble PARSE support

10. [11]Nelson B Bolyard <nelson@bolyard.me> update and complete broadcast and crypto features in sntp

11. [12]Jean-Francois Boudreault <Jean-Francois.Boudreault@viagenie.qc.ca> IPv6 support

12. [13]Reg Clemens <reg@dwf.com> Oncore driver (Current maintainer)

13. [14]Steve Clift <clift@ml.csiro.au> OMEGA clock driver

14. [15]Casey Crellin <casey@csc.co.za> vxWorks (Tornado) port and help with target configuration

15. [16]Sven Dietrich <sven_dietrich@trimble.com> Palisade reference clock driver, NT adj. residuals, integrated Greg's Winnt port.

16. [17]John A. Dundas III <dundas@salt.jpl.nasa.gov> Apple A/UX port

17. [18]Torsten Duwe <duwe@immd4.informatik.uni-erlangen.de> Linux port

18. [19]Dennis Ferguson <dennis@mrbill.canet.ca> foundation code for NTP Version 2 as specified in RFC-1119

19. [20]John Hay <jhay@icomtek.csir.co.za> IPv6 support and testing

20. [21]Dave Hart <davehart@davehart.com> General maintenance, Windows port interpolation rewrite

21. [22]Claas Hilbrecht <neoclock4x@linum.com> NeoClock4X clock driver

22. [23]Glenn Hollinger <glenn@herald.usask.ca> GOES clock driver

23. [24]Mike Iglesias <iglesias@uci.edu> DEC Alpha port

24. [25]Jim Jagielski <jim@jagubox.gsfc.nasa.gov> A/UX port

25. [26]Jeff Johnson <jbj@chatham.usdesign.com> massive prototyping overhaul

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27. [29]Poul-Henning Kamp <phk@FreeBSD.ORG> Oncore driver (Original author)

28. [30]Frank Kardel [31]<kardel (at) ntp (dot) org> PARSE <GENERIC> driver (>14 reference clocks), STREAMS modules for PARSE, support scripts, syslog cleanup, dynamic interface handling

29. [32]William L. Jones <jones@hermes.chpc.utexas.edu> RS/6000 AIX modifications, HPUX modifications

30. [33]Dave Katz <dkatz@cisco.com> RS/6000 AIX port

31. [34]Craig Leres <leres@ee.lbl.gov> 4.4BSD port, ppsclock, Magnavox GPS clock driver

32. [35]George Lindholm <lindholm@ucs.ubc.ca> SunOS 5.1 port

33. [36]Louis A. Mamakos <louie@ni.umd.edu> MD5-based authentication

34. [37]Lars H. Mathiesen <thorinn@diku.dk> adaptation of foundation code for Version 3 as specified in RFC-1305

35. [38]Danny Mayer <mayer@ntp.org>Network I/O, Windows Port, Code Maintenance

36. [39]David L. Mills <mills@udel.edu> Version 4 foundation: clock discipline, authentication, precision kernel; clock drivers: Spectracom, Austron, Arbiter, Heath, ATOM, ACTS, KSI/Odetics; audio clock drivers: CHU, WWV/H, IRIG

37. [40]Wolfgang Moeller <moeller@gwdgv1.dnet.gwdg.de> VMS port

38. [41]Jeffrey Mogul <mogul@pa.dec.com> ntptrace utility

39. [42]Tom Moore <tmoore@fievel.daytonoh.ncr.com> i386 svr4 port

40. [43]Kamal A Mostafa <kamal@whence.com> SCO OpenServer port

41. [44]Derek Mulcahy <derek@toybox.demon.co.uk> and [45]Damon Hart-Davis <d@hd.org> ARCRON MSF clock driver

42. [46]Rob Neal <neal@ntp.org> Bancomm refclock and config/parse code maintenance

43. [47]Rainer Pruy Rainer.Pruy@informatik.uni-erlangen.de monitoring/trap scripts, statistics file handling

44. [48]Dirce Richards <dirce@zk3.dec.com> Digital UNIX V4.0 port

45. [49]Wilfredo Sánchez <wsanchez@apple.com> added support for NetInfo

46. [50]Nick Sayer <mrapple@quack.kfu.com> SunOS streams modules

47. [51]Jack Sasportas <jack@innovativeinternet.com> Saved a Lot of space on the stuff in the html/pic/ subdirectory

48. [52]Ray Schnitzler <schnitz@unipress.com> Unixware1 port

49. [53]Michael Shields <shields@tembel.org> USNO clock driver

50. [54]Jeff Steinman <jss@pebbles.jpl.nasa.gov> Datum PTS clock driver

51. [55]Harlan Stenn <harlan@pfcs.com> GNU automake/autoconfigure makeover, various other bits (see the ChangeLog)

52. [56]Kenneth Stone <ken@sdd.hp.com> HP-UX port

53. [57]Ajit Thyagarajan <ajit@ee.udel.edu>IP multicast/anycast support

54. [58]Tomoaki TSURUOKA <tsuruoka@nc.fukuoka-u.ac.jp>TRAK clock driver

55. [59]Paul A Vixie <vixie@vix.com> TrueTime GPS driver, generic TrueTime clock driver

56. [60]Ulrich Windl <Ulrich.Windl@rz.uni-regensburg.de> corrected and validated HTML documents according to the HTML DTD

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